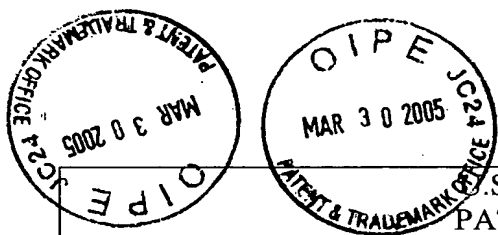


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200U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT		Docket Number: 10746/9	
Application Number 09/376,904	Filing Date August 18, 1999	Examiner David E. ODLAND	Art Unit 2662
Title ATM SWITCH		Applicant(s) Seisho YASUKAWA et al.	

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

1. In accordance with the duty of disclosure under 37 C.F.R. § 1.56 and in conformance with the procedures of 37 C.F.R. §§ 1.97 and 1.98 and M.P.E.P. § 609, attorney(s) for Applicant(s) hereby bring the reference(s) listed on the attached modified PTO Form No. 1449 to the attention of the Examiner. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

2. A copy of each patent, publication or other information listed on the modified PTO form 1449 is enclosed, except as otherwise indicated.

3. Pursuant to 37 C.F.R. § 1.97(c) and § 1.97(e), Applicants' attorney certifies that each item of information contained in the Information Disclosure Statement was first cited in a communication dated January 13, 2005 from a foreign patent office in a counterpart foreign (European) patent application not more than three months prior to the filing of Information Disclosure Statement.

4. It is believed that no fees are due in connection with this Information Disclosure Statement. However, should any fees be due, the Commissioner is authorized to charge Deposit Account No. 11-0600 for such fees. A duplicate copy of this communication is enclosed for charging purposes.

Dated: 3/30/2005

By:

Aaron C. Deitch (Reg. No. 33,865)

KENYON & KENYON

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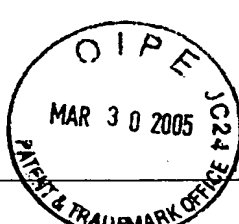
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**SUPPLEMENTAL
INFORMATION DISCLOSURE
STATEMENT BY APPLICANT
PTO-1449**

DOCKET NO.
10746/9

SERIAL NO.
09/376,904

APPLICANT
Seisho YASUKAWA et al.

FILING DATE
August 18, 1999

GROUP
2662

U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT/PUBLICATION NUMBER	PATENT/PUBLICATION DATE	NAME	CLASS	SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						Yes	No
	EP 0 471 380	February 19, 1992	Europe			English Document	

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EXAMINER INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
	Akihiko Takase et al. "Datapath architecture and technology for large scale ATM switching systems" Global Telecommunications Conference, 1996. Globecom '96. Communications: The Key to Global Prosperity London, UK 18-22 Nov. 1996, New York, NY, USA, IEEE, US, 18 November 1996, pages 1395-1399.
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	Hitoshi Obara "Design of a Multistage Self-Routing Switch with a Distributed Cell Sequence Control" Electronics & Communications in Japan, Part I - Communications, Scripta Technica. New York, US, vol. 73, no. 10, 1 October 1990, pages 14-27.
	P.C. Wong et al. "Pipeline banyan - a parallel fast packet switch architecture" Discovering a New World of Communications. Chicago, June 14-18, 1992. Bound Together With B0190700, Vol. 3, Proceedings of the International Conference on Communications, New York, IEEE, US, VOL. 4, 14 June 1992, pages 882-887.
	Syng-Jyan Wang "Load-Balancing in Multistage Interconnection Networks under Multiple-Pass Routing" Journal of Parallel and Distributed Computing, Academic Press, Duluth, MN, US, vol. 36, no. 2, 1 August 1996, pages 189-194.
	European Search Report, issued January 13, 2005.

EXAMINER	DATE CONSIDERED
EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	